

Simplified Resonant DC-DC Converter with High Frequency Drive

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We present a new circuit for resonant dc-dc converter of forward type made of a small number of components which is almost the same in a fly back converter as a smoothing inductor and a free-wheel diode are removed. As parasitic elements like stray capacitors and inductors are operating as a part of a resonant circuit, high frequency switching of low losses can be obtained. A leakage inductance of the transformer is utilized as a inductor of resonant circuit although it prevents the output power going through the transformer in conventional converters.

Key words: resonant converter, high frequency switching, Leakage inductance, leakage transformer

1. Introduction

Magnetizing inductor of transformer is utilized for the resonant inductor in all of the already developed circuits of resonant dc-dc converters using the output transformers. (1) As the switching frequency becomes higher for the aids of light weight and small size, the output transformer is constructed by low permeability core of small loss in high frequency operation or without core. In this case, the power transmitted through transformer is limited by increment of the leakage inductor. We can realize a new converter utilizing the leakage inductors instead of magnetizing one by means of connecting another resonant capacitor to the secondary winding of the transformer. This capacitor is only one component adding to the fly back converter. However, there is a clear difference in polarity of transformer windings between this converter and the fly back one.

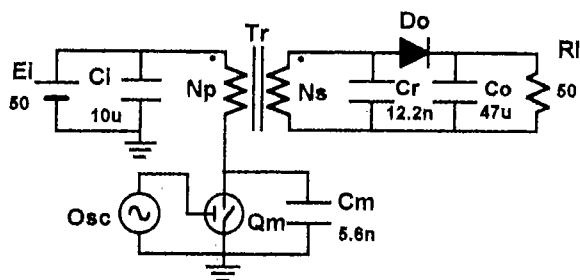


Fig.1 Simplified DC-DC Converter

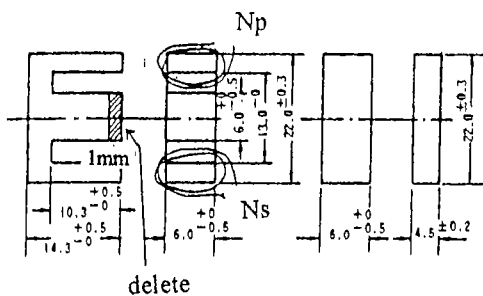


Fig.2 Leakage Transformer

We present an experimental circuit of simplified dc-dc converter in Fig.1. E_i is a dc power source. C_i is an input capacitor, that is, a bypath capacitor of high frequency current. Tr is a leakage transformer with the primary winding N_p and the secondary one N_s . Q_m is a power MOSFET as a main switch driven by an oscillator Osc . C_m is a main resonant capacitor. Cr is a resonant capacitor. Do is a first recovery diode. Co is an output capacitor smoothing the output voltage delivered to a load resistor R_l . Fig.2 shows the construction of the leakage transformer Tr . A magnetic core (E-I type ferrite core) has a leakage path with air gap between N_p and N_s .

2. Analysis

Fig.3 is the equivalent circuit for analysis of the experimental one shown in Fig.1. C_i is eliminated as the output impedance of E_i is zero. L_p and L_s are the leakage inductances of N_p side and N_s side respectively. L_e and R_e are the magnetizing inductance and the equivalent resistor of power loss of Tr . C_m includes a drain capacity of Q_m and a stray capacity of N_p . D_m operates sufficiently in this circuit although it isn't a first recovery diode because reverse voltage isn't applied when D_m is ON state. Values of L_s , Cr , Do , Co and R_l are equivalent ones transferred from the secondary N_s side to the primary N_p side.

Fig.4 shows wave forms given by SCAT simulator. (2) v_{Qm} and v_{Cr} in (a) are voltages of the main switch Q_m and the resonant capacitor Cr respectively. $i_{Qm}+i_{Dm}$ in (b) are currents of the main switch (Q_m and D_m) and the output diode Do .

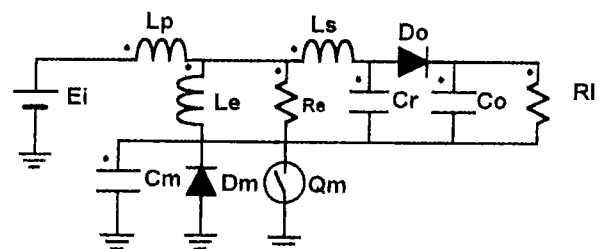


Fig.3 The Equivalent Circuit for Analysis

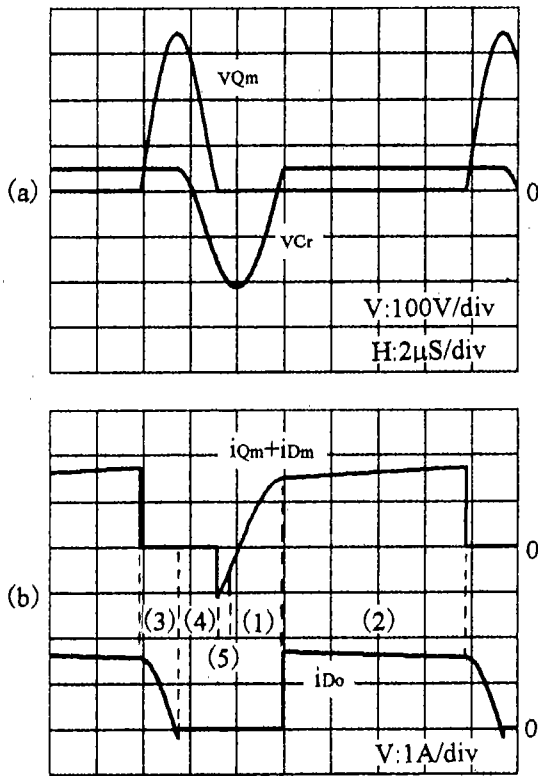


Fig.4 Voltage and Current Wave Forms in Simulation

- (a) v_{Qm} : Voltage of Q_m
- v_{Cr} : Voltage of C_r
- (b) $i_{Qm}+i_{Dm}$: Current of MOSFET
- i_{Do} : Current of Do

$L_p = L_s = 100\mu H$ $L_e = 1mH$
 $C_m = 5nF$ $C_r = 8.2nF$ $C_o = 47\mu F$
 $F_s = 72kHz$ $T_{off} = 3.8\mu S$
 $E_i = 50V$ $R_l = 47.2\Omega$

Fig.5 shows a transition of states in one switching cycle. State (1) to (5) are corresponding to period (1) to (5) in Fig.4 (b). In state (1), load current of R_l is delivered from C_o . At the same time, current i_{Qm} of Q_m increases as the dc source E_i is applied to L_p , L_s and C_r . Alternative current of L_e can negligible as $L_e \gg L_p$ and L_s . Voltage v_{Cr} of resonant capacitor C_r increases by means of charging by i_{Qm} . When v_{Cr} becomes equal to the output voltage, Do turns ON and state changes to state (2). Current from E_i flows through L_p , L_s , Do , C_o/R_l and Q_m . State transition from (2) to (3) happens when Q_m turns ON. As the voltage of C_m is zero in state (2), v_{Cm} , that is, v_{Qm} increases from zero. v_{Cm} increases by charging current through E_i , L_p , L_s , Do and C_o/R_l . At the instant when v_{Cm} becomes the maximum voltage and current i_{Do} is zero, Do turns OFF and transition from (3) to (4) occurs. C_m is discharged through C_r , L_s , L_p and E_i . When v_{Cm} becomes zero, D_m turns ON and transition from state (4) to state (5) occurs. i_{Dm} approaches to zero in this state and D_m is still in ON. While D_m is ON, Q_m turns ON and transition from state (5) to state

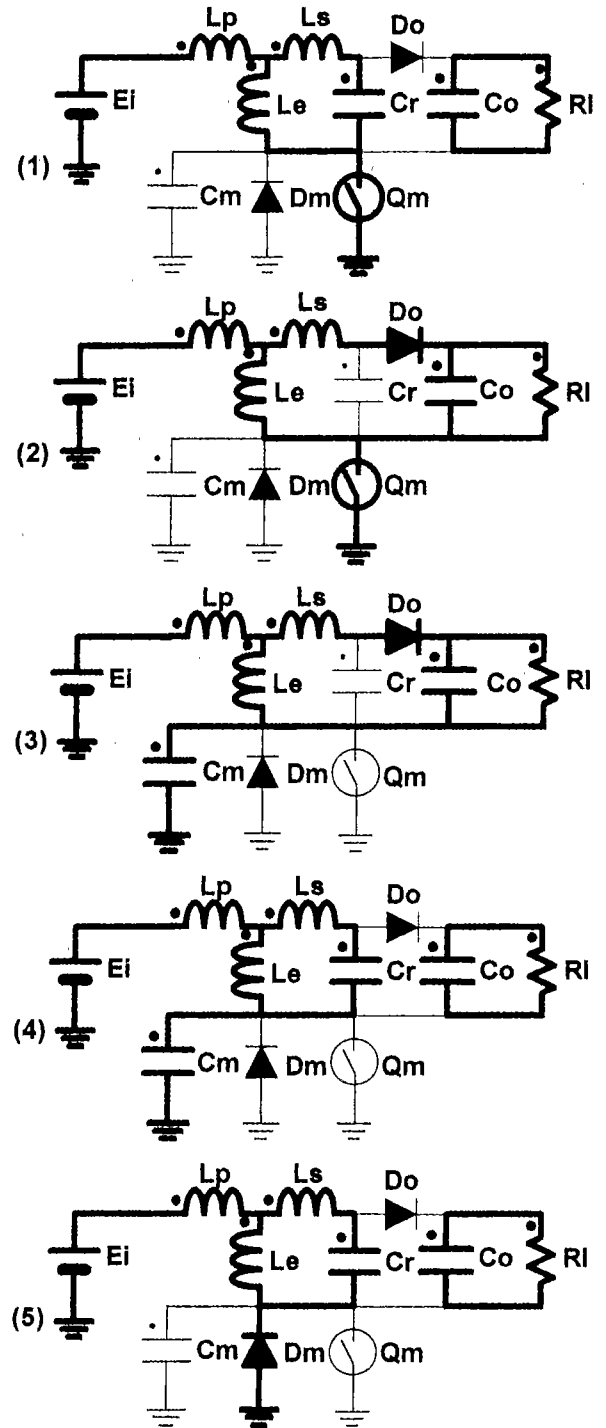


Fig.5 Transition of states in One Switching Cycle

- (1) Q_m is ON still Do is OFF.
- (2) Do is ON.
- (3) Q_m is OFF still Do is ON.
- (4) Do is OFF.
- (5) D_m is ON.

(1) occurs. Precisely speaking, when i_{Qm} is negative, Q_m and D_m are ON. As D_m has, however, a forward voltage drop, the great part of the current passes through Q_m of the small

resistance.

As the result of one switching cycle mentioned above, the turn-on loss of Q_m is extremely small as Q_m turns ON when v_{Qm} is zero. And the turn-off loss of Q_m is small as v_{Qm} arises from zero voltage and Q_m turns OFF rapidly.

3. Magnetization of Core

We consider magnetization of transformer's core in comparison with our circuit and fly back converter. The magnetization current i_{Le} in our circuit shown in Fig.6 (a) is the difference of the input current i_i and the output current i_o .

On the other hand, i_{Le} in the fly back converter shown in Fig.6 (b) is the sum of i_i and i_o . Therefore, the magnetization current in our circuit is smaller than that in the fly back converter. Moreover, we can make the dc magnetization current zero in the next condition. We get

$$\eta E_i I_i = E_o I_o \quad \text{--- (1)}$$

where η : conversion efficiency,

E_i : dc input voltage,

I_i : dc input current,

E_o : dc output voltage

and I_o : dc output current.

In Fig.6 (a), we get the next expression on the dc magnetization current I_{Le} .

$$I_{Le} = I_i - I_o \quad \text{--- (2)}$$

From expression (1) and (2), we get

$$I_{Le} = I_i (1 - \eta E_i / E_o) \quad \text{--- (3)}$$

When we make $E_o = \eta E_i$, I_{Le} becomes zero. That is, the operating point of the transformer's core is the center of B-H characteristics.

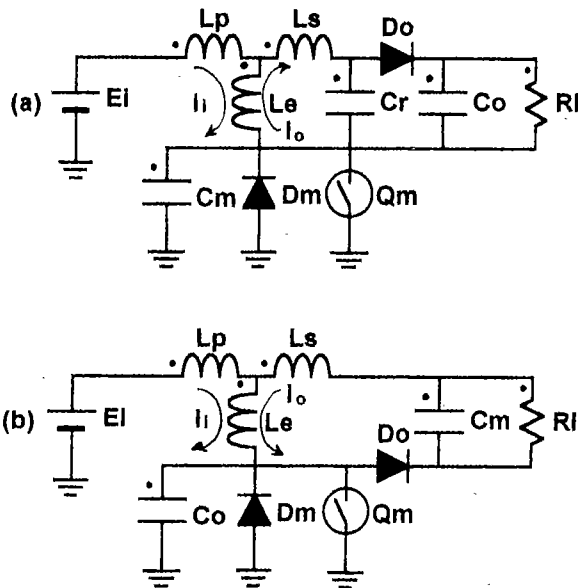


Fig.6 Magnetizing Current of Transformer's Core

(a) New Circuit

(b) Fly Back Converter

4. Experimental Results

The experimental circuit is shown in Fig.1. In this circuit, $E_i = 50V$, $E_o = 44V$, $C_m = 5.6nF$, $C_r = 12.2nF$, $C_o = 47\mu F$. We use a E-I type ferrite core (TDK:FEI-22/PC40) for the transformer Tr shown in Fig.2 which is usually used in the converter of 15W output power. And $L_p = L_s = 20\mu H$ and $L_e = 200\mu H$ are given by winding 17 turns for each and by adjustable deleting the center magnetic path. Semiconductor switches are Q_m : Power MOSFET 2SK1500 (NEC) and D_o : Fast Recovery Diode ESAD95-4 (Fuji Elec.).

Fig.7 shows voltage and current wave forms of Q_m and D_o .

Fig.8 shows the experimental results on the switching frequency F_s and the conversion efficiency η vs. the output current I_o . In this experiment, F_s is controlled as keeping the output voltage $E_o = 44V$. We can get a high conversion efficiency $\eta \approx 90\%$ in the output current range from 0.5A to 2.4A.

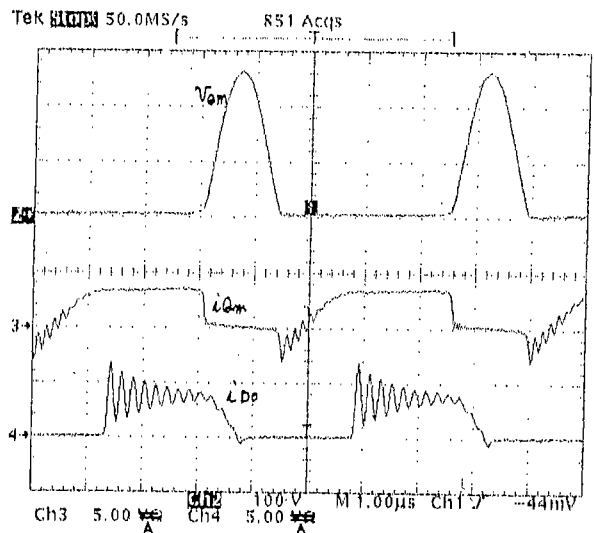


Fig.7 Voltage and Current Wave Forms of Q_m and D_o

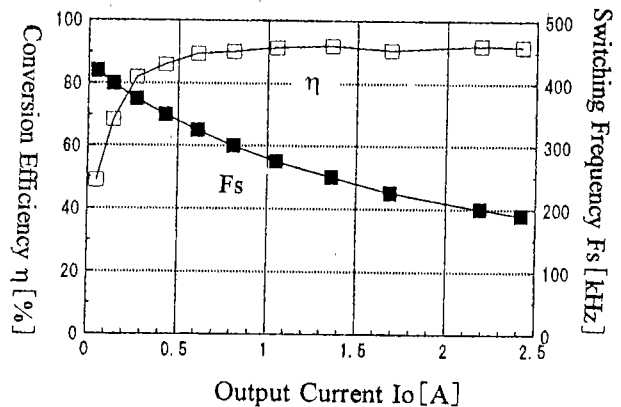


Fig.8 Switching Frequency F_s and Conversion Efficiency η vs. Output Current I_o

Fig.9 shows the non-saturation region and saturation region of the transformer's core vs. the output current I_o . The output voltage E_o is adjustable without saturation from E_{omin} to E_{omax} . We can see that the saturation of the core occurs in the operating range far from $E_o = 44V$, that is, the condition of the expression (3).

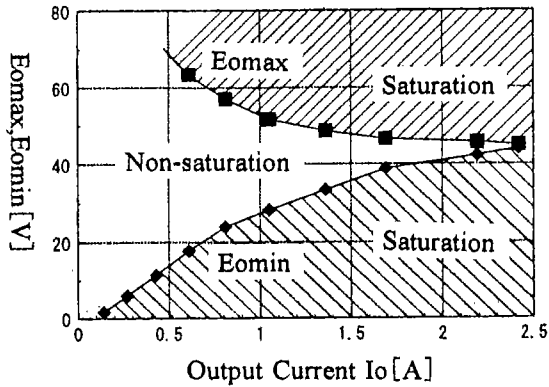


Fig.9 Non-saturation Region and Saturation Region

5. Conclusion

We can realize a new dc-dc converter with a small number of components by means of utilizing leakage inductances of the transformer. All of the parasitic elements are included in the equivalent circuit like stray capacitors, stray inductors and capacitors between electrodes of switching elements. Therefore, this converter can operate in high switching frequency with high efficiency of small size and light weight. This is suitable for an onboard converter supplied by a dc bus line with a little voltage change or by the output of a switching rectifier with unity power factor.

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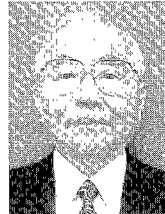
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